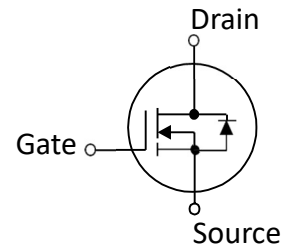
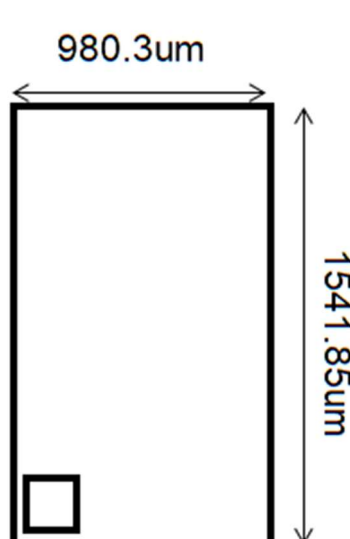


40V, 68A ⁽¹⁾ N-Channel MOSFET

- Proprietary Trench Gate Device Design and Processes
- High Reliability Capability
- Sampled CP Probing and Inking

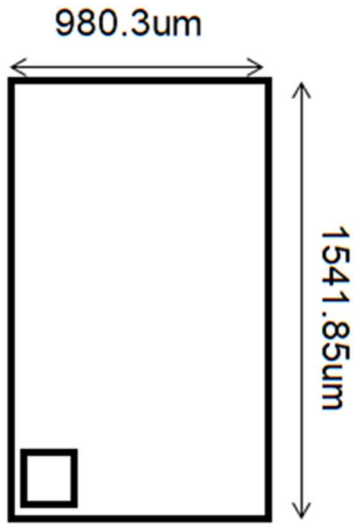
SYMBOL

Electrical Characteristics in C/P Test (T_J at 25 °C)

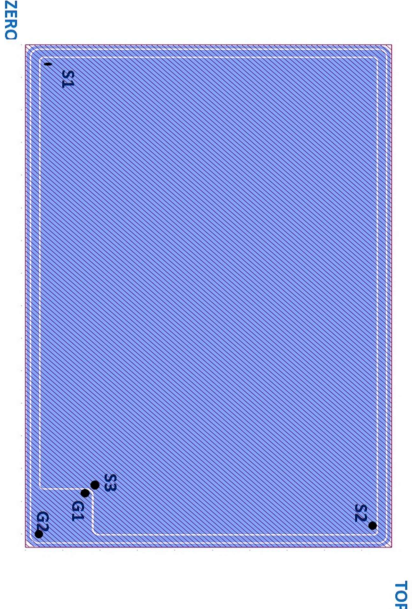
Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Condition
V _{(BR)DSS}	Drain-Source Breakdown Voltage	40	—	—	V	V _{GS} = 0V, I _D = 250μA
R _{DS(ON)}	Static Drain-Source On-Resistance	—	4.7	7.4	mΩ	V _{GS} = 10V, I _D = 1A ⁽²⁾
R _{DS(ON)}	Static Drain-Source On-Resistance	—	7.4	9.6	mΩ	V _{GS} = 4.5V, I _D = 1A ⁽²⁾
V _{GS(th)}	Gate Threshold Voltage	1	—	2.5	V	V _{DS} = V _{GS} , I _D = 250μA
I _{DSS}	Drain-to-Source Leakage Current	—	—	1	μA	V _{DS} = 40V, V _{GS} = 0V
I _{GSS}	Gate-to-Source Leakage Current	-100	—	100	nA	V _{DS} = 0V, V _{GS} = ±20V
T _J , T _{STG}	Operating and Storage Temperature	-55°C to 150°C Max.				

Mechanical Data		Die Drawing
Chip Size	980 μm X 1542 μm	
Gate Pad Size	160 μm X 160 μm	
Source Pad Size	897 μm X 1458 μm	
Scribe Line Width	60 μm	
Wafer Thickness	150 μm	
Wafer Diameter	200 mm	
Gross Die	17362 EA	
Source Metallization	Al-Cu (4μm typical)	
Drain Metallization	Ti-Ni-Ag	
Passivation	N/A	
Recommended Storage Environment	Store in original container, in dry nitrogen, 6 months at ambient temperature of 23°C ± 3°C	

(1) This characteristic assumes the die is assembled in SOP-8 package. Actual performance may degrade when assembled.

(2) Pulse Width t_p = < 1 mS, Duty Cycle < 2%.

Specific Assembly Information Bill of Material (BOM)		Die Drawing
Package Type	SOP-8	
Die Attach Method	Soft solder	
Soft Solder Composition	Pb,Sn,Ag	
Gate Wire Bonding	Cu, 2 mil x1	
Source Wire Bonding	Cu, 2 mil x8	
Molding Compound Manufacturer	G700HF	
Solder Plating Composition	Pure Tin	

Position			Bonding Diagram Top View
	X (μm)	Y (μm)	
ZERO	0	0	
TOP	1541.85	980.3	
S1	42.6	42.6	
S2	1490.25	928.7	
S3	1348.88	192.8	
G1	1374.88	166.6	
G2	1515.25	26.6	
G3			

Electrical Characteristics in F/T Test (T_J at 25 °C)						
Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Condition
I_{DSS}	Drain-to-Source Leakage Current	—	—	1	μA	$V_{DS} = 40V, V_{GS} = 0V$
I_{GSSF}	Gate-to-Source Leakage Current	—	—	100	nA	$V_{DS} = 0V, V_{GS} = +20V$
I_{GSSR}	Gate-to-Source Leakage Current	-100	—	—	nA	$V_{DS} = 0V, V_{GS} = -20V$
BV_{DSS}	Drain-Source Breakdown Voltage	40	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
BV_{DSS}	Drain-Source Breakdown Voltage	40	—	—	V	$V_{GS} = 0V, I_D = 1mA$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	—	—	9.5	m Ω	$V_{GS} = 10V, I_D = 10A$
$V_{GS(th)}$	Gate Threshold Voltage	1	—	2.5	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
V_{SD}	Body Diode Forward Voltage	—	—	1.1	V	$V_{GS} = 0V, I_{SD} = 10A$
I_{AS}	Avalanche Current				A	$V_{DD} = 40V, V_{GS} = 10V, R_G = 25\Omega, L = 0.5mH$
T_J, T_{STG}	Operating and Storage Temperature	-55	—	150	$^{\circ}C$	

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