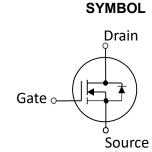


## 30V N-Channel MOSFET

- Advanced Split Gate Device Design and Processes
- High Reliability Capability
- Sampled CP Probing



Electrica	Electrical Characteristics in C/P Test (TJ at 25 °C)					
Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Condition
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	30	—	—	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance		17.7	22.4	mΩ	$V_{GS} = 10V, I_{D} = 1A(1)$
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance		24.7	30	mΩ	$V_{GS}$ =4.5V, $I_{D}$ =1A(1)
V <sub>GS (th)</sub>	Gate Threshold Voltage	1		2.5	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA
I <sub>DSS</sub>	Drain-to-Source Leakage Current	_	—	1	μA	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V
I <sub>GSS</sub>	Gate-to-Source Leakage Current	-100		100	nA	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V
Tj, Tstg	Operating and Storage Temperature	-55°C to 150°C Max.				

Mechanical Data	Die Drawing	
Chip Size <sup>(2)</sup>	786 µm X 579 µm	
Gate Pad Size	201 µm X 204 µm	
Source Pad Size	586 µm X 379 µm	786.35 um
Scribe Line Width	60 µm	et sol
Wafer Thickness	150 µm	201 um
Wafer Diameter	200 mm	
Gross Die	53958 EA	596,35 um
Source Metallization	Al-Cu (4µm typical)	
Drain Metallization	Ti-Ni-Ag	
Passivation	SiN	
Recommended Storage Environment	Store in original container, in dry nitrogen, 6 months at ambient temperature of 23°C ± 3°C	

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(1) Pulse Width tp = < 1 mS, Duty Cycle < 2%.

(2) Chip size not include scribe line.



Specific Assembly Info	Die Drawing			
Package Type	DFN5*6			
Die Attach Method	Soft solder	786.35 um		
Soft Solder Composition	Pb,Sn,Ag	un 621 un 721 um		
Gate Wire Bonding	Cu, 2 mil x 1	1599 um		
Source Wire Bonding	Cu, 2 mil x 5	596.35 um		
Molding Compound Manufacturer	G700HF	U		
Solder Plating Composition	Pure Tin			

	Position		Bonding Diagram Top View
	X (μm)	Υ (μm)	
ZERO	0	0	
ТОР	786.35	579	53
S1	100	100	
S2	686.35	288.298	51
S3	498.975	479	31
G1	548.975	338.298	ZERO
G2	749.975	542.298	



Electrica	I Characteristics in F/T Test	(TJ at	t <b>25</b> °C	:)		
Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Condition
I <sub>DSS</sub>	Drain-to-Source Leakage Current	_		1	μA	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V
I <sub>GSSF</sub>	Gate-to-Source Leakage Current	_		100	nA	V <sub>DS</sub> =0V, V <sub>GS</sub> =+20V
I <sub>GSSR</sub>	Gate-to-Source Leakage Current	-100			nA	V <sub>DS</sub> =0V, V <sub>GS</sub> =-20V
$BV_{DSS}$	Drain-Source Breakdown Voltage	30			V	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	30	—	_	V	$V_{GS}$ =0V, $I_D$ =1mA
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	_	—	25	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =20A
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	_	—	33	mΩ	V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A
$V_{GS\ (th)}$	Gate Threshold Voltage	3	—	5	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA
$V_{\text{SD}}$	Body Diode Forward Voltage	_	—	1.2	V	V <sub>GS</sub> =0V, I <sub>SD</sub> =20A
I <sub>AS</sub>	Avalanche Current				Α	$V_{DD}$ =30V, $V_{GS}$ =10V, $R_{G}$ =25 $\Omega$ , L=0.1mH
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature	-55	_	150	°C	

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