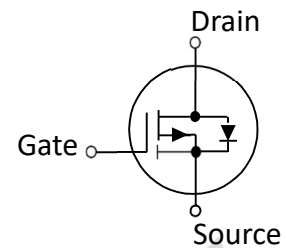


40V P-Channel MOSFET

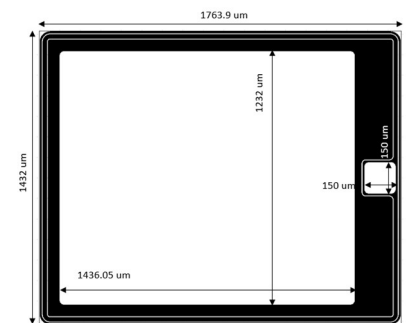
- Advanced Trench Device Design and Processes
- High Reliability Capability
- Sampled CP Probing and Inking

SYMBOL

Electrical Characteristics in C/P Test (T_J at 25 °C)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Condition
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	—	—	-40	V	$V_{GS} = 0V, I_D = -250\mu A$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	—	12	14.2	m Ω	$V_{GS} = 10V, I_D = -1A^{(2)}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	—	16	18.6	m Ω	$V_{GS} = 4.5V, I_D = -1A^{(2)}$
$V_{GS(th)}$	Gate Threshold Voltage	-2.2	—	-1.2	V	$V_{DS} = V_{GS}, I_D = -250\mu A$
I_{DSS}	Drain-to-Source Leakage Current	-1	—	—	μA	$V_{DS} = -40V, V_{GS} = 0V$
I_{GSS}	Gate-to-Source Leakage Current	-100	—	100	nA	$V_{DS} = 0V, V_{GS} = \pm 20V$
T_J, T_{STG}	Operating and Storage Temperature	-55°C to 150°C Max.				

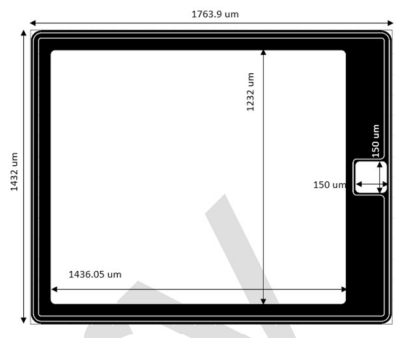
Mechanical Data
Die Drawing

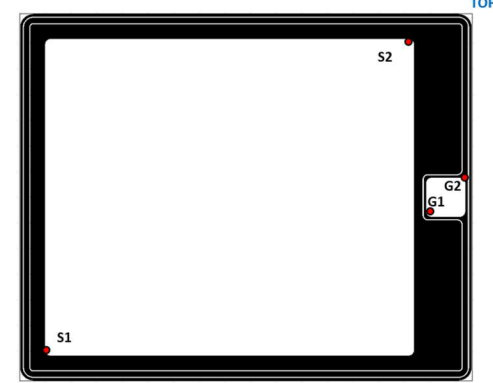
Chip Size	1763 μm X 1432 μm
Gate Pad Size	150 μm X 150 μm
Source Pad Size	1436 μm X 1232 μm
Scribe Line Width	60 μm
Wafer Thickness	150 μm
Wafer Diameter	200 mm
Gross Die	10613 EA
Source Metallization	Al-Cu (4 μm typical)
Drain Metallization	Ti-Ni-Ag
Passivation	SiN
Recommended Storage Environment	Store in original container, in dry nitrogen, 6 months at ambient temperature of 23°C \pm 3°C



(1) This characteristic assumes the die is assembled in DFN5*6 package. Actual performance may degrade when assembled.

(2) Pulse Width $t_p = < 1$ mS, Duty Cycle $< 2\%$.

Specific Assembly Information Bill of Material (BOM)		Die Drawing
Package Type	DFN5*6	
Die Attach Method	Soft solder	
Soft Solder Composition	Pb,Sn,Ag	
Gate Wire Bonding	Cu, 2 mil x1	
Source Wire Bonding	Al Ribbon	
Molding Compound Manufacturer	G700HF	
Solder Plating Composition	Pure Tin	

Position			Bonding Diagram Top View
	X (um)	Y (um)	
ZERO	0	0	
TOP	1763.9	1432	
S1	100	100	
S2	1536.05	1332	
G1	1586.05	641	
G2	1736.05	791	

Electrical Characteristics in F/T Test (T_J at 25 °C)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Condition
I_{DSS}	Drain-to-Source Leakage Current	-1	—	—	μA	$V_{DS} = -40V, V_{GS} = 0V$
I_{GSSF}	Gate-to-Source Leakage Current	—	—	100	nA	$V_{DS} = 0V, V_{GS} = +20V$
I_{GSSR}	Gate-to-Source Leakage Current	-100	—	—	nA	$V_{DS} = 0V, V_{GS} = -20V$
BV_{DSS}	Drain-Source Breakdown Voltage	—	—	-40	V	$V_{GS} = 0V, I_D = -250\mu A$
BV_{DSS}	Drain-Source Breakdown Voltage	—	—	-40	V	$V_{GS} = 0V, I_D = -1mA$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	—	—	16	m Ω	$V_{GS} = -10V, I_D = -8A$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	—	—	21	m Ω	$V_{GS} = -4.5V, I_D = -6A$
$V_{GS(th)}$	Gate Threshold Voltage	-2.2	—	-1.2	V	$V_{DS} = V_{GS}, I_D = -250\mu A$
V_{SD}	Body Diode Forward Voltage	-1.2	—	—	V	$V_{GS} = 0V, I_{SD} = -8A$
T_J, T_{STG}	Operating and Storage Temperature	-55	—	150	$^{\circ}C$	

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